



COPY

PATENT
81754.0061

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Noriyuki SARUHASHI, et al.

Serial No: 09/871,596

Filed: May 30, 2001

For: PHYSICAL LAYER DEVICE TESTING
METHOD, PHYSICAL LAYER DEVICE
WITH TEST CIRCUITS, AND
TRANSMITTING/RECEIVING CIRCUIT
WITH TEST CIRCUITS

Art Unit: 2133

Examiner: Joseph D. Torres

I hereby certify that this correspondence
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PETITION FOR EXTENSION OF TIME

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 C.F.R. 1.136, Applicant respectfully petitions the
Commissioner for a one-month extension of time extending to July 26, 2004
(July 24, 2004 being a Saturday), the period for response to the final Office Action
dated March 24, 2004. A check for \$110 is enclosed. The responsive paper(s) are
attached.

Please charge any insufficiency or credit any overpayment to Deposit Account
No. 50-1314. A copy of this petition is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: July 26, 2004

By: 

Anthony J. Orler

Registration No. 41,232

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